



P8388a

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Masahiko Mizuta, et al. Group Art Unit: 2113
Serial No.: 10/649,153 Examiner: Robert W. Beausoliel
Filed: August 27, 2003
Title: Semiconductor Device and In-Circuit Emulator Using the Same

CERTIFICATE OF MAILING

I hereby certify that this correspondence, is on this date, being deposited with the United States Postal Service with sufficient postage as "First Class" mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
Date: October 18, 2005


Ann F. George


STATUS REQUEST

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that Applicant(s) attorneys be advised of the current status of the above-identified application.

Respectfully submitted,



Mark P. Watson
Registration No. 31,448

Please address all correspondence to:
Epson Research and Development, Inc.
Intellectual Property Department
150 River Oaks Parkway, Suite 225
San Jose, CA 95134
Customer No. 20178
Phone: (408) 952-6000
Facsimile: (408) 954-9058
Date: October 18, 2005